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STATUS REQUEST		Docket Number CM03422J	
In re Application of STENGEL, ROBERT ET AL.			
Application Number	10/036,550	File Date	12/21/01
Title		A METHOD AND STRUCTURE FOR INTEGRATED CIRCUIT INTERFERENCE ISOLATION ENHANCEMENT	
Group Art Unit	2811	Examiner	N/A

Status of the above-identified application is requested.  
The last paper(s) received by Applicant(s) was:

☒ Official Filing Receipt  
☐ Other -

The last paper(s) submitted by Applicant(s) was:

Since the transmittal/receipt of said paper(s), no other communications have been received from the United States Patent and Trademark Office.

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Date	4/8/04		

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first-class mail in an envelope addressed to the Mail Stop: Status Inquiry, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313.

Name	Maria E. Rodriguez		
Signature	<i>Maria E. Rodriguez</i>	Date	4/8/04